

QEX (ISSN: 0886-8093) is published bimonthly in January, March, May, July, September, and November by the American Radio Relay League, 225 Main Street, Newington, CT 06111-1494. Periodicals postage paid at Hartford, CT and at additional mailing offices.

POSTMASTER: Send address changes to: QEX, 225 Main St, Newington, CT 06111-1494 Issue No 279

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Subscription rate for 6 issues:

In the US: ARRL Member \$24,
nonmember \$36;

US by First Class Mail:
ARRL member \$37, nonmember \$49;

International and Canada by Airmail: ARRL member
\$31, nonmember \$43;

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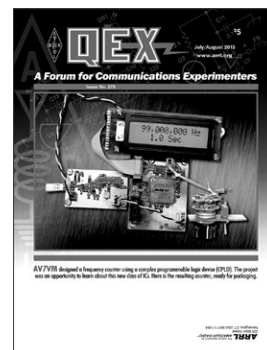
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About the Cover

Gary Richardson, AA7VM, wanted a better frequency counter than a previous project. He thought about using a field programmable gate array (FPGA) but realized that a complex programmable logic device (CPLD) would be better suited to the task. The project was an opportunity to learn about this new (to him) class of ICs, so he began reading about them. The resulting frequency counter is described in his article, "Another Frequency Counter for the Experimenter."



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